

What is claimed is:

1. A liquid crystal display device comprising:

a plurality of gate lines;

5 a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, at least one of said data lines defining the pixel region having a first data line section and a second data line section, the first data line section and the second data line section intersecting at a data line bent portion;

at least one data electrode in the pixel region, the data electrode having a first data electrode section and a second data electrode section, the first data electrode section and the second data electrode section intersecting at a data
15 electrode bent portion;

at least one common electrode in the pixel region, the common electrode having a first common electrode section and a second common electrode section, the first common electrode section and the second common electrode section intersecting
20 at a common electrode bent portion; and

at least one common line in the pixel region, the common electrode crossing the data lines, the data electrode, and the common electrode.

2. The liquid crystal display device of claim 1, wherein the common line crosses the data line at the data line bent portion, the data electrode at the data electrode bent portion, and the common electrode at the common electrode bent portion.

3. The liquid crystal display device of claim 1, wherein the common line is substantially parallel to the gate line.

4. The liquid crystal display device of claim 1, wherein the first data line section, the first data electrode section, and the first common electrode section are substantially parallel.

5. The liquid crystal display device of claim 1, wherein the common lines comprise a transparent conductive material.

6. The liquid crystal display device of claim 1, wherein the data electrodes comprise a transparent conductive material.

7. The liquid crystal display device of claim 1, wherein the common electrodes comprise a transparent conductive material.

8. The liquid crystal display device of claim 1, wherein the data electrodes partially overlap at least one of the gate lines.

9. The liquid crystal display device of claim 1, wherein a storage capacitor is formed where the common line crosses the data electrode.

10. A liquid crystal display device comprising:
a plurality of gate lines;
a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, at least one of said data lines defining the pixel region, the data lines having a plurality of data line segments, the data line segments intersecting at data line bent portions;

at least one data electrode in the pixel region, the data electrode having a plurality of data electrode segments, the data electrode segments intersecting at common electrode bent portions; and

at least one common electrode in the pixel region, the common electrode having a plurality of common electrode segments, the segments intersecting at common electrode bent

5x10
6x10
7

portions; and

at least one light shielding layer on the pixel region,
the data electrode, and the common electrode at respective
ones of the data line bent portions, the data electrode bent
5 portions and the common electrode bent portions.

11. The device of claim 10, wherein one of the light
shielding layers is a common line.

12. An in-plane switching mode liquid crystal display device
comprising:

gate lines formed on a substrate;

data lines having a plurality of bent portions to cross the
gate lines, the data and gate lines defining a pixel region;

15 a plurality of data electrodes and common electrodes having
a plurality of bent portions;

a common line on the bent portions of the data lines, the
data electrodes and the common electrodes; and

20 a plurality of auxiliary common lines on the bent portions
of the data electrodes and the common electrodes.

13. The device of claim 12, wherein the common lines are formed
in parallel with the gate lines.

14. The device of claim 12, wherein the common lines include transparent conductive film.

15. The device of claim 12, wherein the data electrodes include transparent conductive film.

16. The device of claim 12, wherein the common electrodes include transparent conductive film.

17. The device of claim 12, wherein at least one of the common lines is formed within the pixel region.

18. The device of claim 12, wherein the data lines, the data electrodes, and the common electrodes have at least one bent portion.

19. The device of claim 12, wherein the common lines are formed with the common electrodes.

20. The device of claim 12, wherein the data electrodes are connected with the gate lines.

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21. The device of claim 12, wherein the data electrodes overlap the gate lines.

5 22. The device of claim 12, wherein the data and common electrodes are on different layers.

23. The device of claim 12, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes.

24. An in-plane switching mode liquid crystal display device, comprising:

gate lines on a substrate;

15 data lines having a plurality of bent portions to cross the gate lines, the data and gate lines defining a pixel region;

a plurality of data electrodes having a plurality of bent portions;

common electrodes having a plurality of bent portions, the

20 common electrodes being connected with each other at the bent portions; and

common lines on the bent portions of the data lines, the data electrodes and the common electrodes.

25. The device of claim 24, wherein the data electrodes are connected with the gate lines.

5 26. The device of claim 24, wherein the common electrodes are connected with the common lines.

27. The device of claim 24, wherein the data electrodes include a first electrode and a second electrode.

28. The device of claim 27, wherein the first electrode has a plurality of bent portions.

29. The device of claim 27, wherein the first electrode is
15 connected with the second electrode.

30. The device of claim 24, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes.